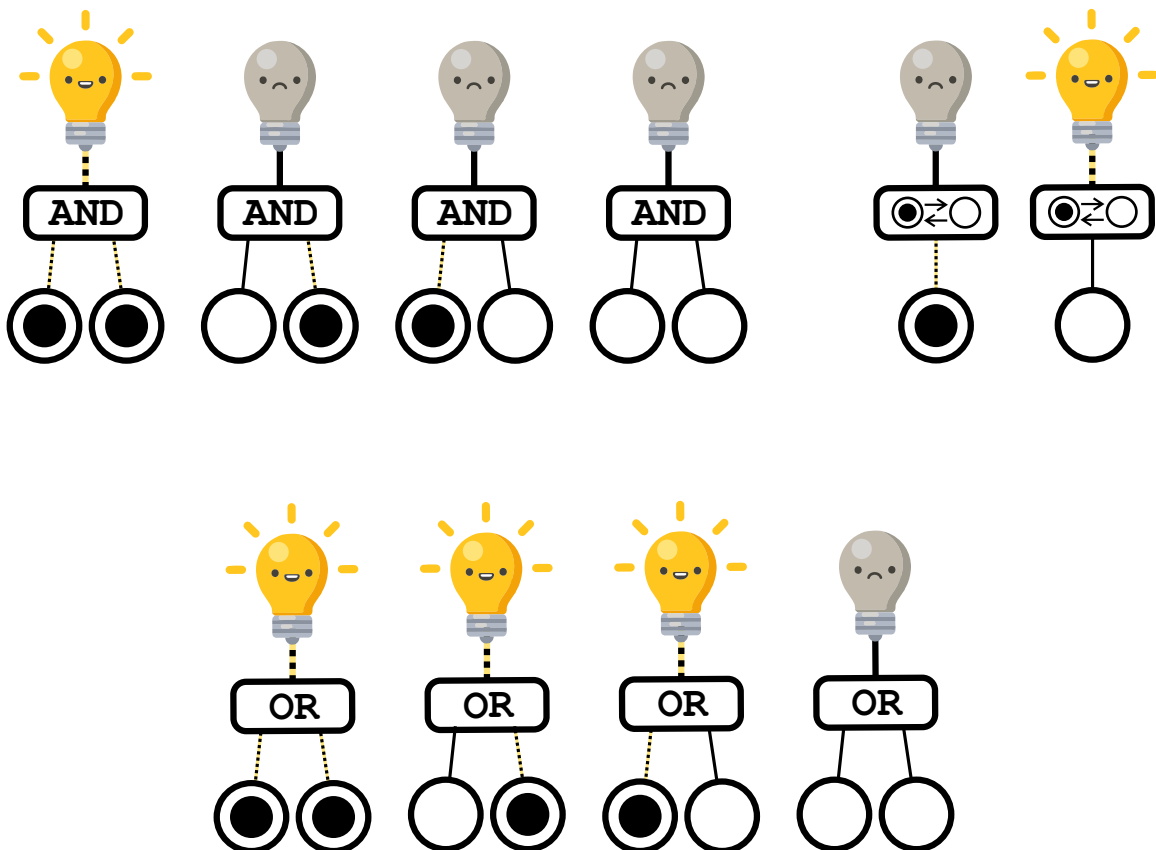
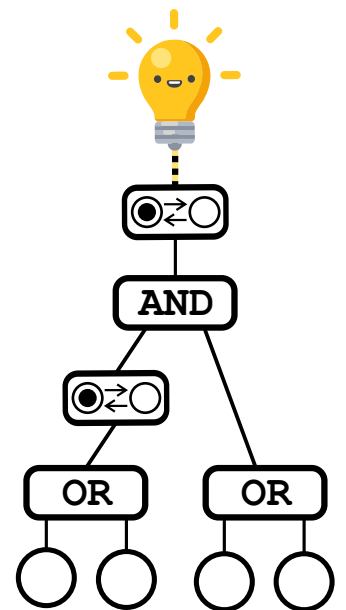
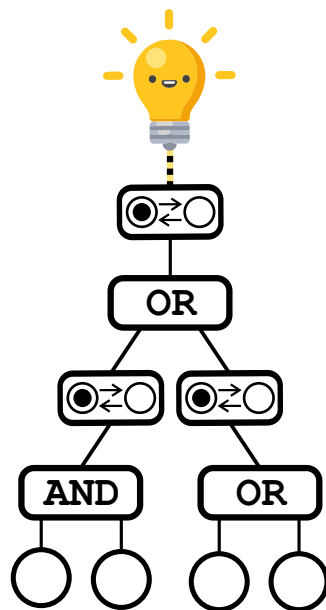
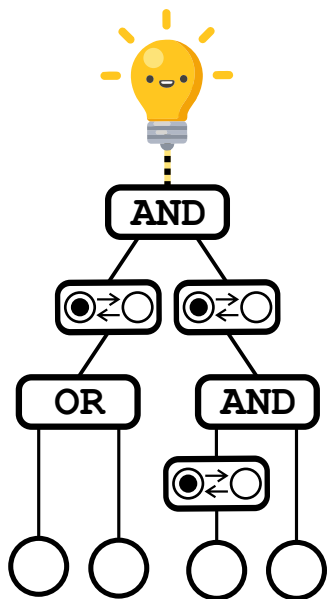
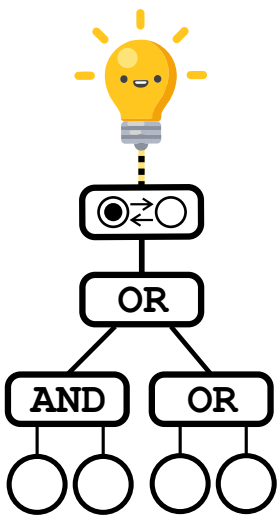
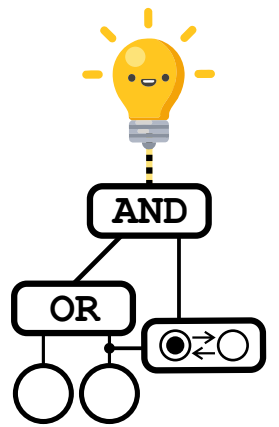
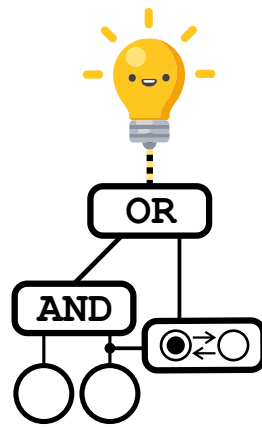
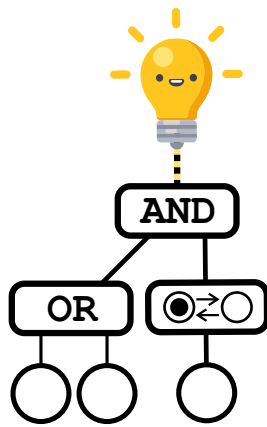
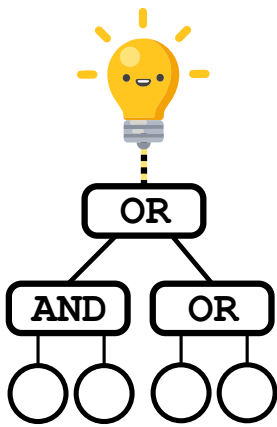
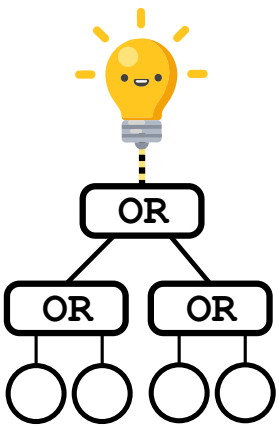
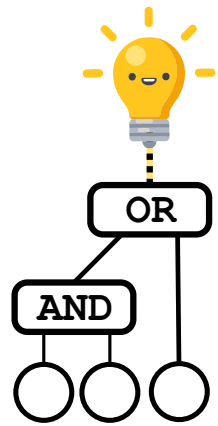
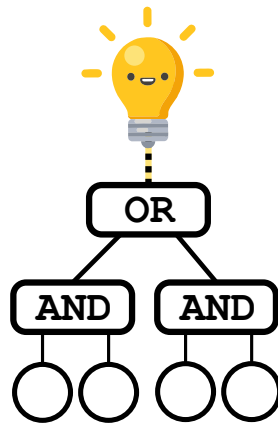
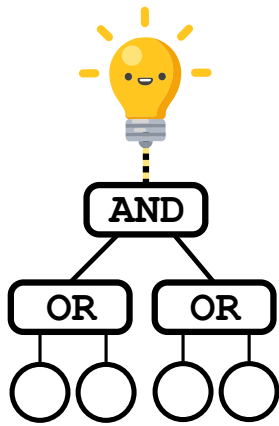
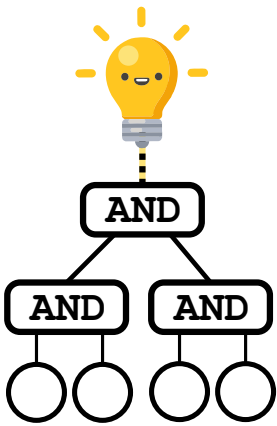


PORTAS LÓGICAS

Rev. 2

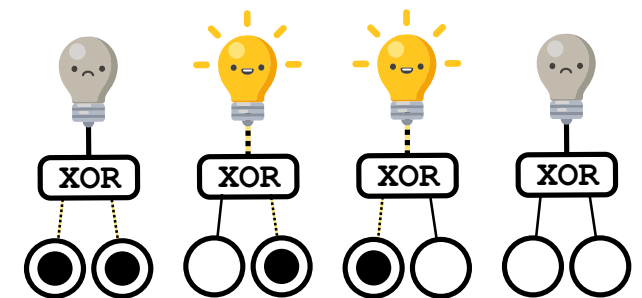
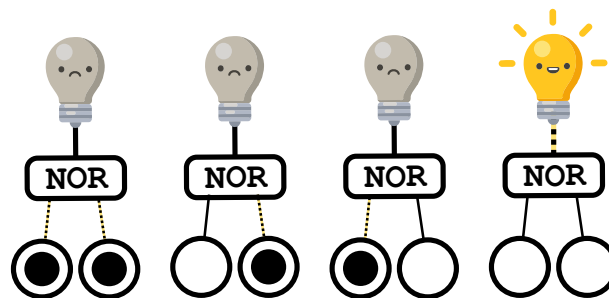
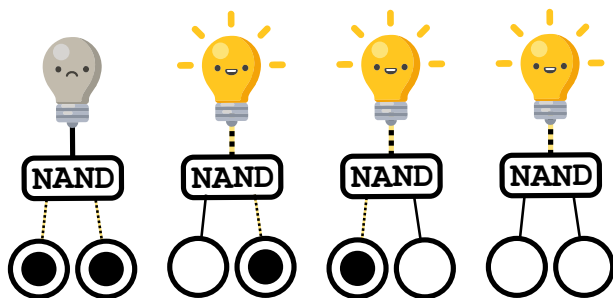
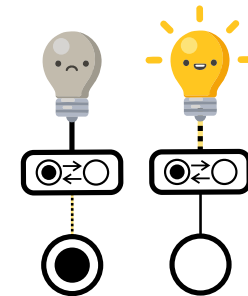
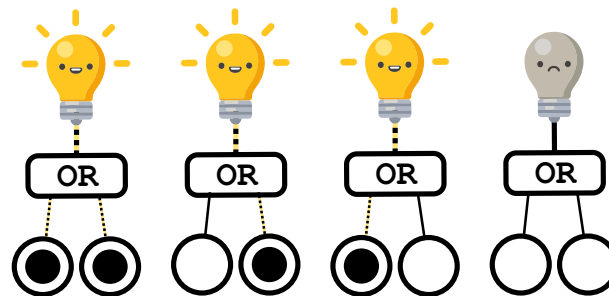
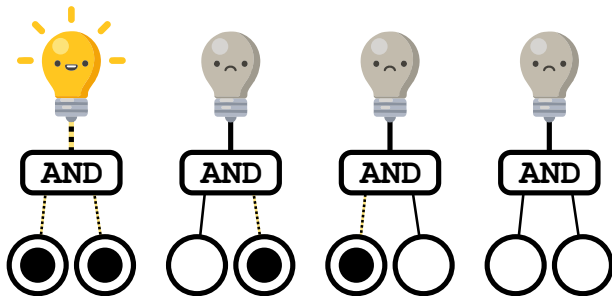
RESUMO DAS REGRAS (Básico)





PORTAS LÓGICAS

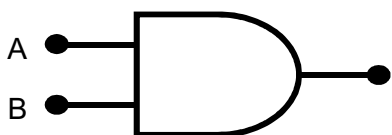
RESUMO DAS REGRAS (Avançado)



PORTAS LÓGICAS

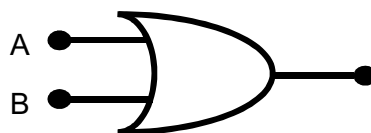
RESUMO DAS REGRAS (Representação Gráfica)

E (AND)



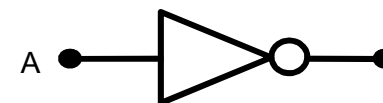
A	B	Saída
0	0	0
0	1	0
1	0	0
1	1	1

OU (OR)



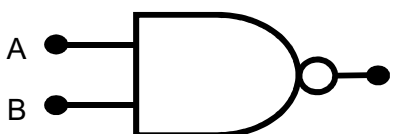
A	B	Saída
0	0	0
0	1	1
1	0	1
1	1	1

NÃO (NOT) - Inversor



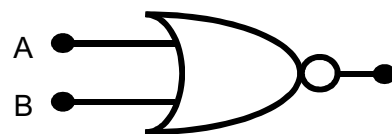
A	Saída
0	1
1	0

NE (NAND)



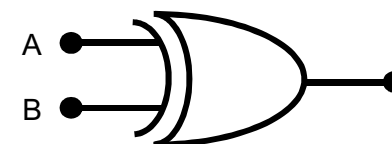
A	B	Saída
0	0	1
0	1	1
1	0	1
1	1	0

NOU (NOR)



A	B	Saída
0	0	1
0	1	0
1	0	0
1	1	0

XOU (XOR)



A	B	Saída
0	0	0
0	1	1
1	0	1
1	1	0